

DSG-SoLID R&D Meeting Minutes

Date: February 25, 2022

Time: 11:00 – 12:00

Attendees: Mary Ann Antonioli, Aaron Brown, Pablo Campero, Brian Eng, George Jacobs, Tyler Lemon, Marc McMullen, and Amrit Yegneswaran

1. Cable fabrication and labeling

Pablo Campero and Mindy Leffel

1. Grounded eleven signal conditioning modules' backplanes to the rack
2. Completed 35 of 64, 100' cables
 - Not soldering old load sensor cables to new cables, as previously decided; connectors will be used instead
3. Labeled valve control panel switches

2. Instrumentation rack debugging

Pablo Campero, Mindy Leffel, and Marc McMullen

1. Debugged and tested voltmeter used to monitor valve position of LVDT module in local mode
2. Tested constant current source board spare for PT-102 temperature sensor
3. Researching testing plugs to facilitate testing all terminal block connections while energized

3. HMI screens development

Pablo Campero

1. Testing CCR-Expert HMI screen monitoring and controls features
2. Adding trends for temperatures